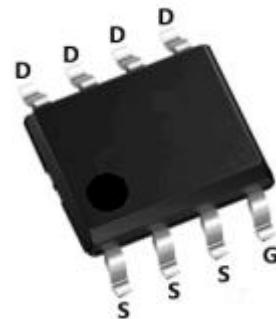
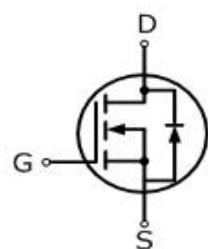


## 4N10PL

4 Amps, 100 Volts N-CHANNEL MOSFET

### FEATURE

- 4A, 100V,  $R_{DS(ON)MAX}=130\text{m}\Omega$  @  $V_{GS}=10\text{V}/1\text{A}$   
 $R_{DS(ON)MAX}=150\text{m}\Omega$  @  $V_{GS}=4.5\text{V}/1\text{A}$
- Low gate charge
- Low  $C_{iss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



SOP-8 PIN CONFIGURATION

### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	4N10PL	UNIT
Drain-Source Voltage	$V_{DSS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	4	A
Pulsed Drain Current (Note 1)	$I_{DM}$	16	
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	18	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	260	°C

### Thermal Characteristics

Parameter	Symbol	4N10PL	Units
Thermal resistance, Junction to Case	$R_{th(J-c)}$	24	°C/W
Maximum Power Dissipation	$T_c=25^\circ\text{C}$	$P_D$	W

Electrical Characteristics ( $T_c=25^\circ\text{C}$ ,unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	100	—	—	V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	$\mu\text{A}$
Gate-Body Leakage Current,Forward	$\text{I}_{\text{GSSF}}$	$\text{V}_{\text{GS}}=20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	100	nA
Gate-Body Leakage Current,Reverse	$\text{I}_{\text{GSSR}}$	$\text{V}_{\text{GS}}=-20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	-100	nA
<b>On Characteristics</b>						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	1.0	—	2.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1\text{A}$	—	100	130	$\text{m}\Omega$
	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=1\text{A}$	—	110	150	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	—	995	—	pF
Output Capacitance	$\text{C}_{\text{oss}}$		—	36	—	pF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		—	32	—	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DS}}=50\text{V}, \text{R}_G=10\Omega,$ $\text{V}_{\text{GS}}=10\text{V}$	—	6.2	—	ns
Turn-On Rise Time	$t_r$		—	8.6	—	ns
Turn-Off Delay Time	$t_{\text{d(off)}}$		—	36.6	—	ns
Turn-Off Fall Time	$t_f$		—	13.2	—	ns
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=80\text{V}, \text{I}_D=1\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$	—	22	—	nC
Gate-Source Charge	$\text{Q}_{gs}$		—	4.5	—	nC
Gate-Drain Charge	$\text{Q}_{gd}$		—	4.5	—	nC
<b>Drain-Source Body Diode Characteristics and Maximum Ratings</b>						
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_S=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_{\text{DD}}=30\text{V}, \text{I}_F=1\text{A},$ $d\text{I}_F/dt=100\text{A/us}, (\text{Note3})$	—	18	—	ns
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$		—	14	—	nC

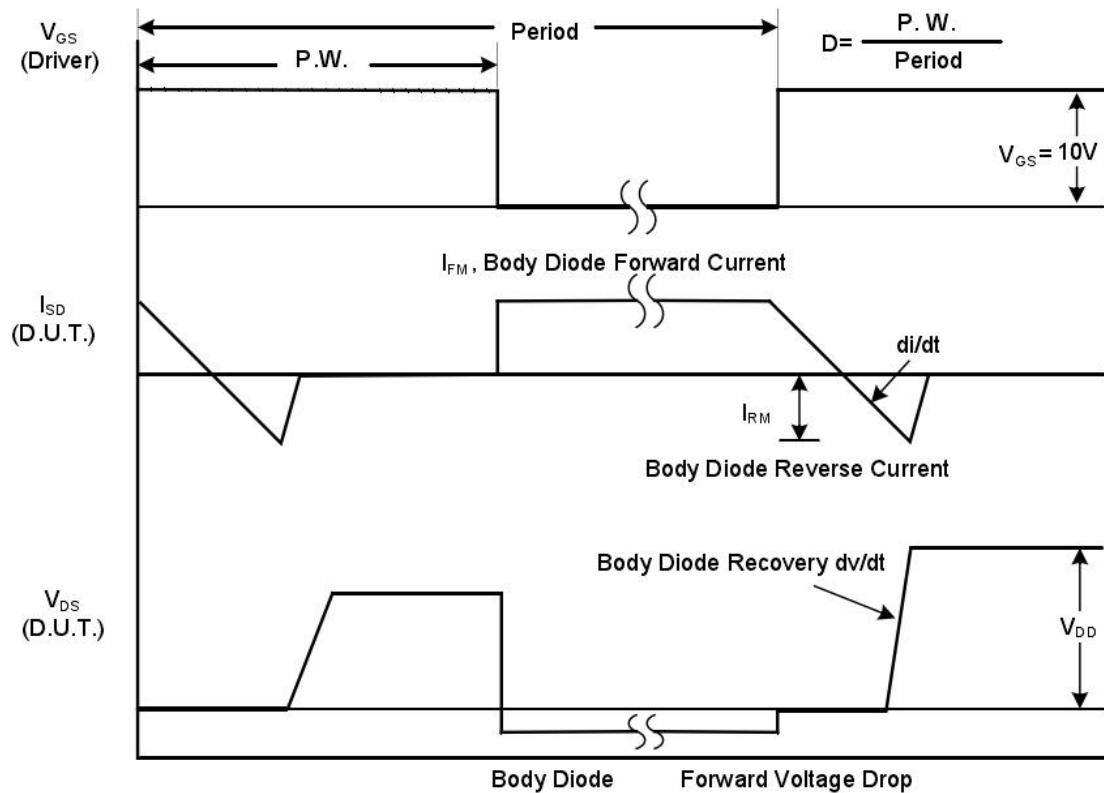
#### Notes

1. Repetitive Rating:pulse width limited by maximum junction temperature.
2. L=0.5mH, $R_g=25\Omega$ , starting  $T_J=25^\circ\text{C}$ .
3. Pulse width $\leq 300\text{us}$ ;duty cycle $\leq 2\%$ .

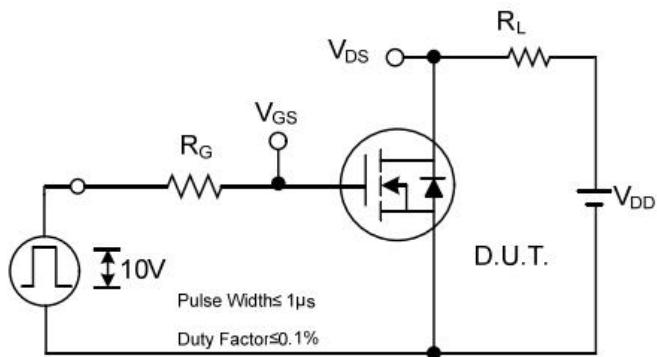
## RATING AND CHARACTERISTIC CURVES



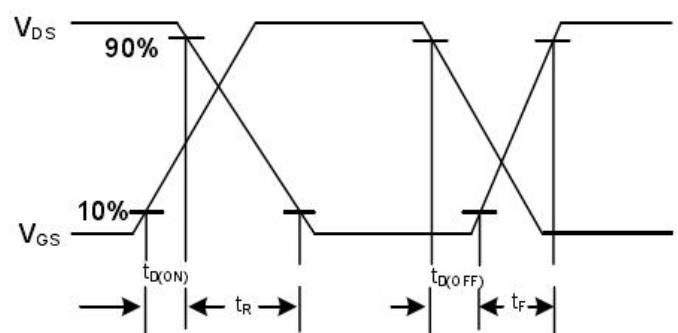
Peak Diode Recovery dv/dt Test Circuit



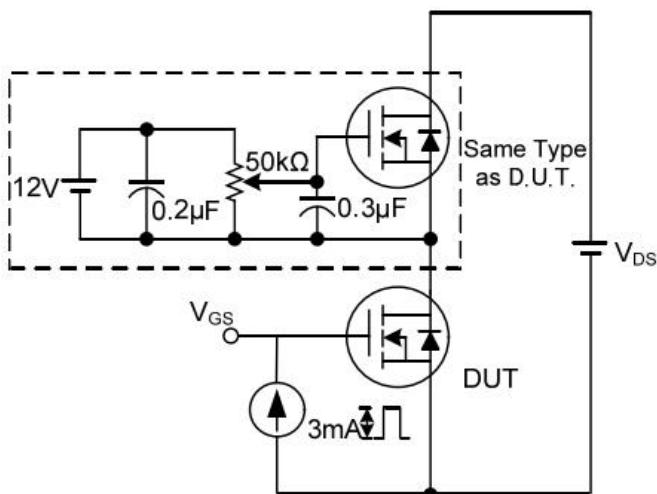
Peak Diode Recovery dv/dt Waveforms



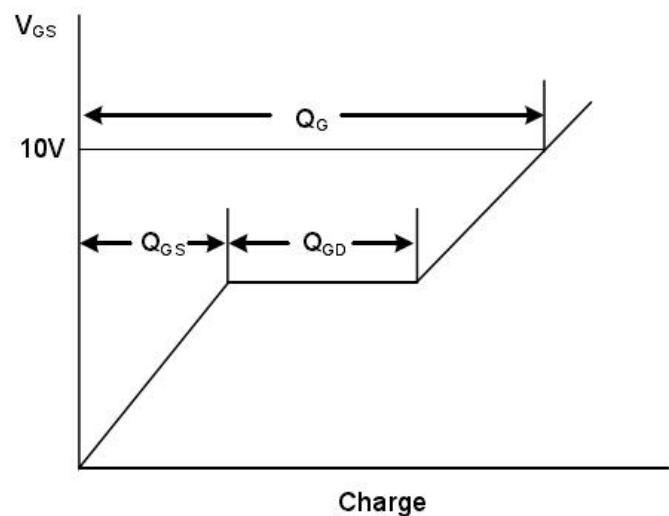
Switching Test Circuit



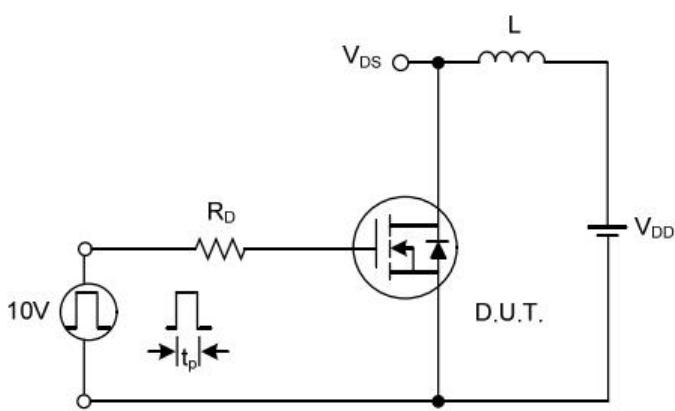
Switching Waveforms



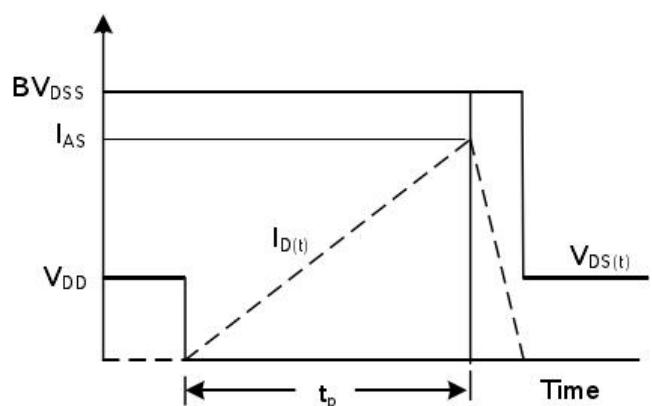
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

## RATING AND CHARACTERISTIC CURVES

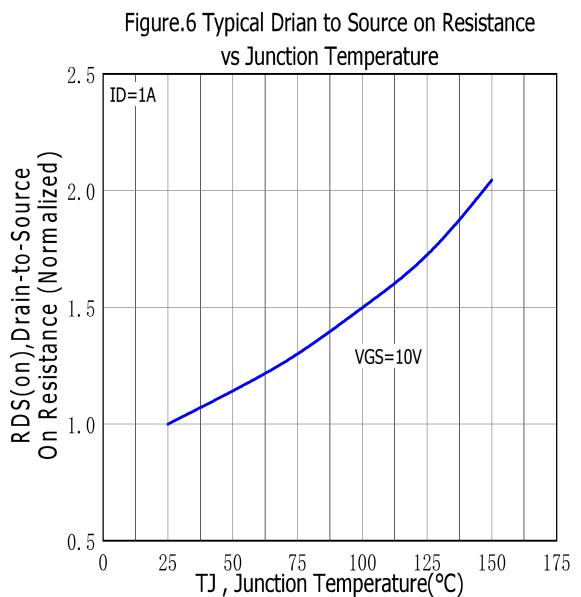
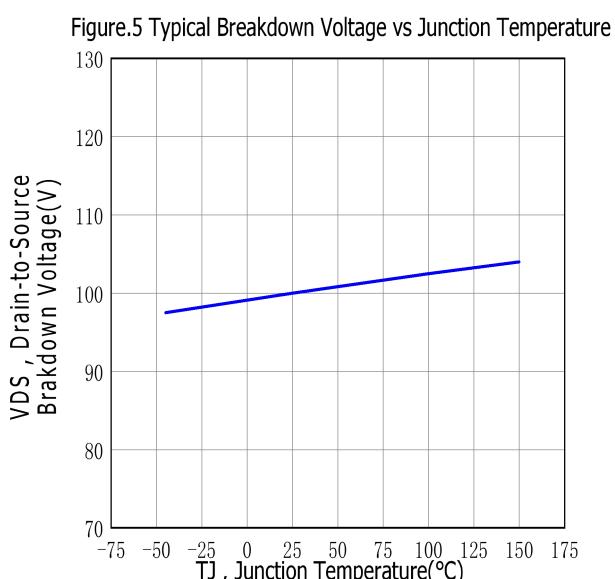
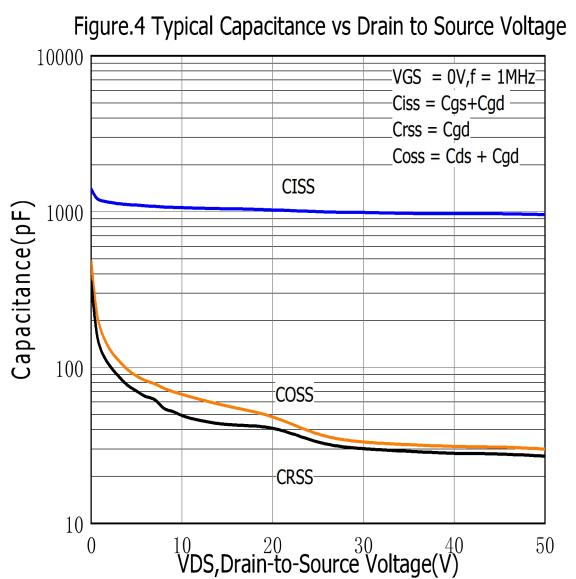
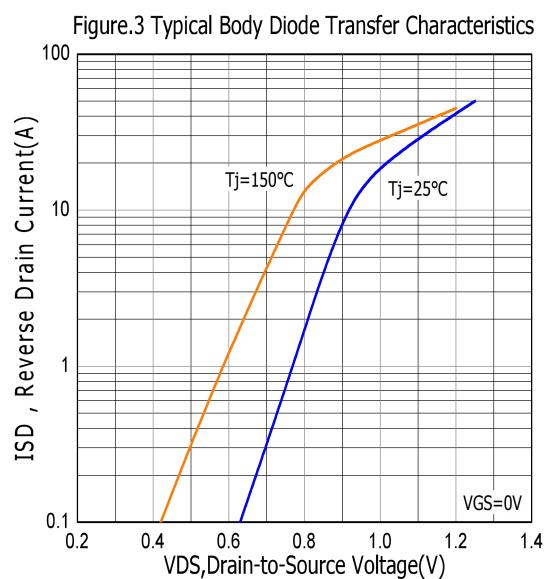
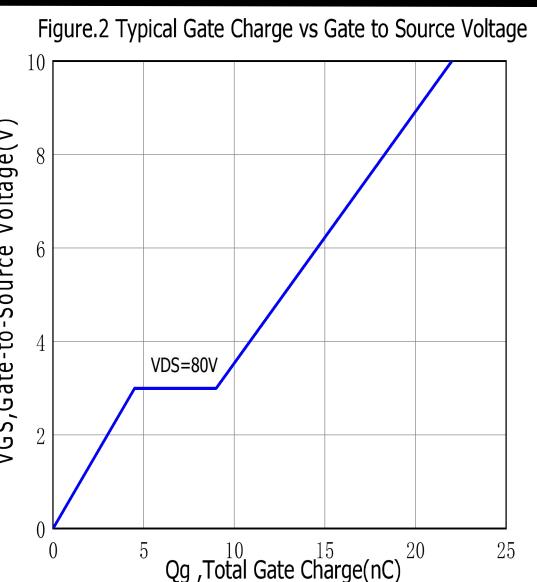
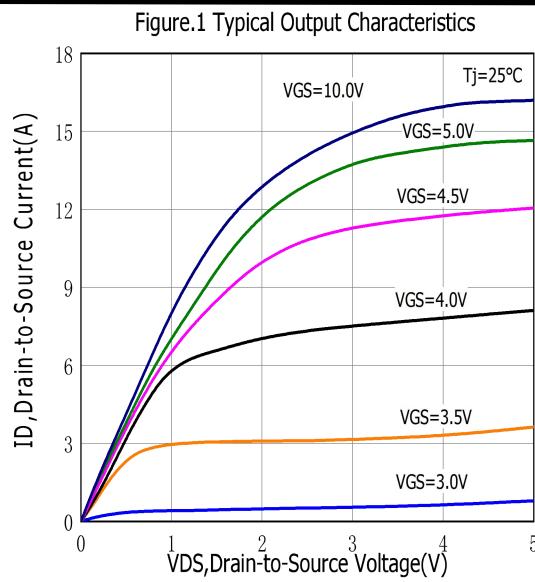


Figure.7 Maximum Forward Bias Safe Operating Area

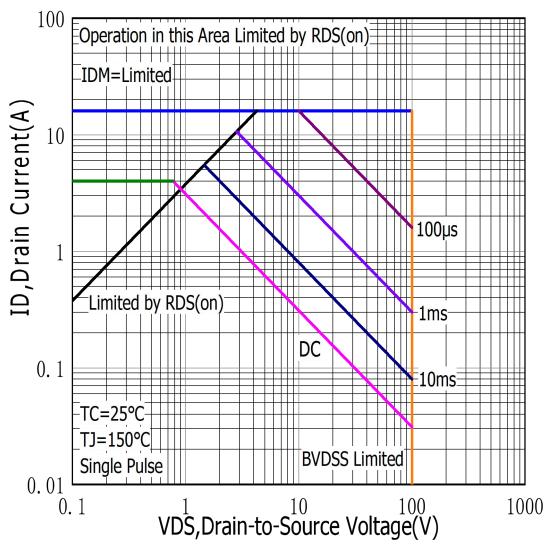


Figure.9 Maximum EAS vs Channel Temperature

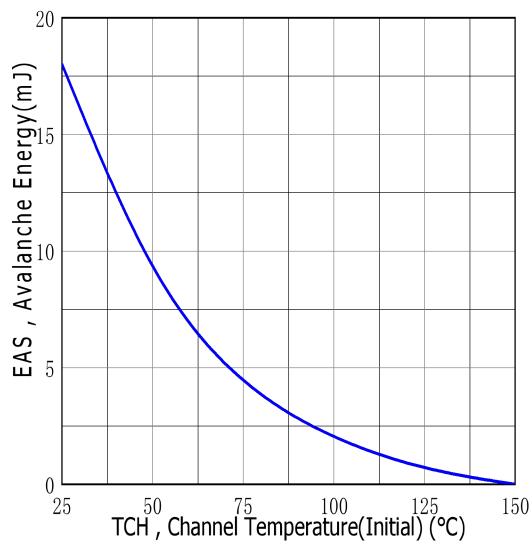


Figure.11 Maximum Effective Thermal Impedance , Junction to Case

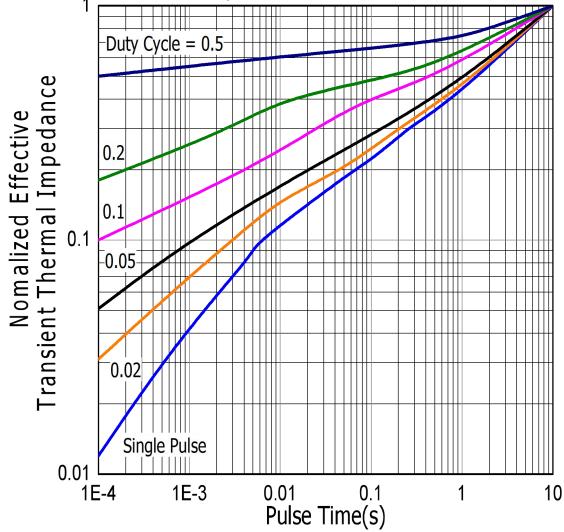


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

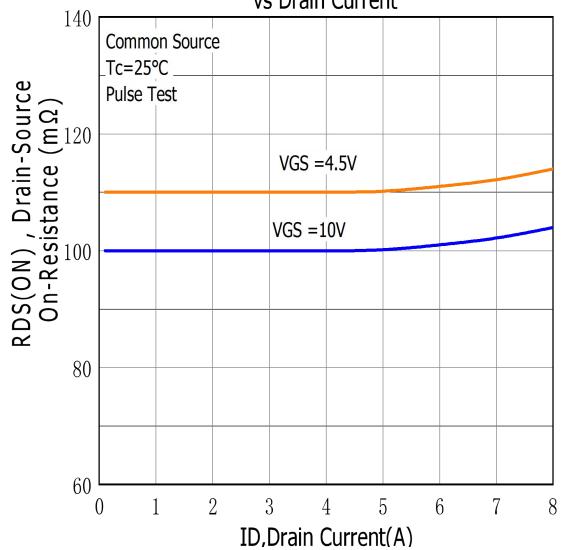


Figure.10 Typical Threshold Voltage vs Case Temperature

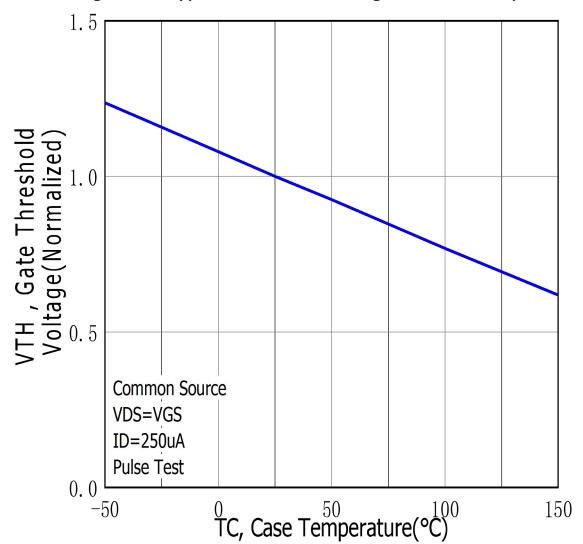
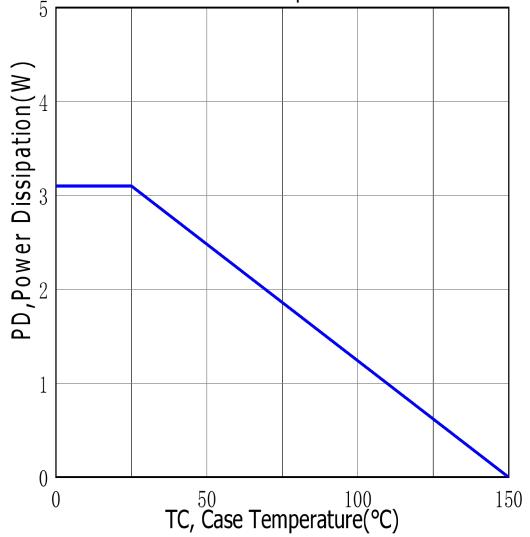
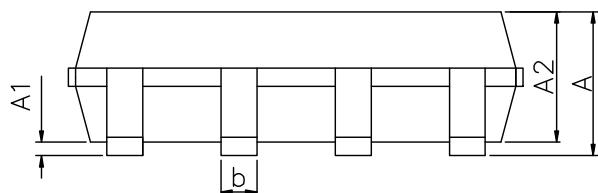
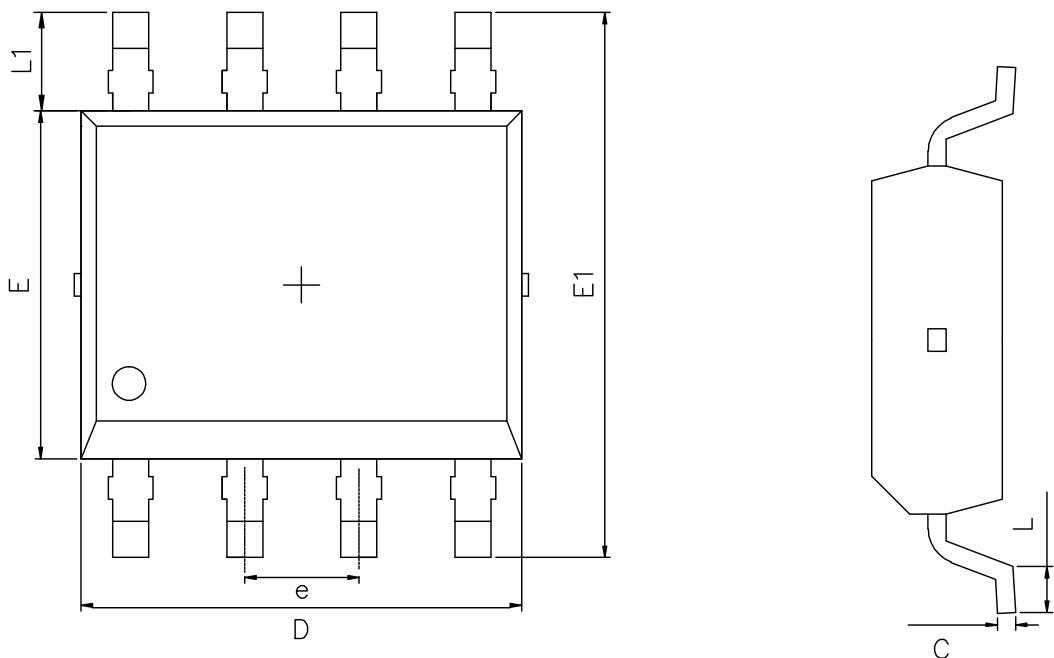


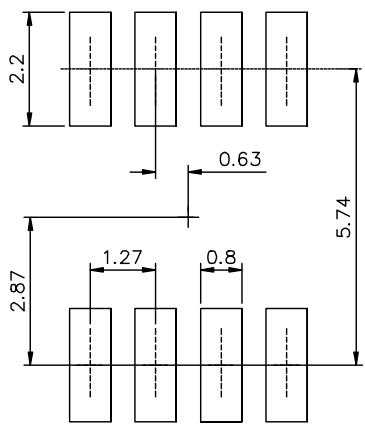
Figure.12 Maximum Power Dissipation vs Case Temperature



## SOP-8 PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	1.35	1.65	1.75
A1	0.10	0.15	0.25
A2	1.25	1.45	1.65
b	0.30	0.40	0.50
c	0.17	0.20	0.25
D	4.80	4.90	5.10
E	3.72	3.87	4.02
E1	5.95	6.10	6.25
e		1.27BCS	
L	0.40	0.65	0.90
L1	0.92	1.07	1.22