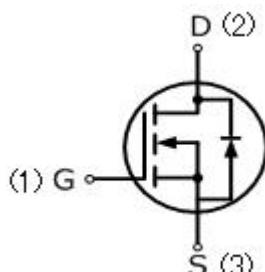


## 5N65F

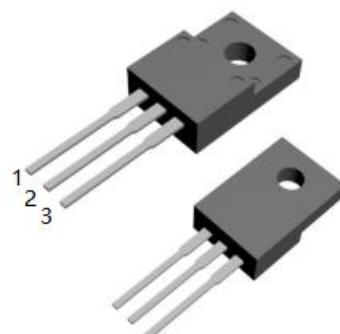
5 Amps, 650 Volts N-CHANNEL Power MOSFET

### FEATURE

- 5A, 650V,  $R_{DS(ON)MAX}=2.8\ \Omega$  @  $V_{GS}=10V/2A$
- Low gate charge
- Low  $C_{iss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-220F-3L



### Absolute Maximum Ratings ( $T_c=25^\circ C$ , unless otherwise noted)

Parameter	Symbol	5N65F	UNIT
Drain-Source Voltage	$V_{DSS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	
Continuous Drain Current	$I_D$	5	A
Pulsed Drain Current (Note 1)	$I_{DM}$	20	
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	150	mJ
Reverse Diode $dV/dt$ (Note 3)	$dv/dt$	5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	260	°C

Parameter	Symbol	5N65F	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	4.1	°C/W
Maximum Power Dissipation	$T_c=25^\circ C$	$P_D$	W

Electrical Characteristics ( $T_c=25^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	650	—	—	V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=650\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	uA
Gate-Body Leakage Current, Forward	$\text{I}_{\text{GSSF}}$	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	100	nA
Gate-Body Leakage Current, Reverse	$\text{I}_{\text{GSSR}}$	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	-100	nA
<b>On Characteristics</b>						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	2.0	—	4.0	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2\text{A}$	—	2.3	2.8	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	—	628	—	pF
Output Capacitance	$\text{C}_{\text{oss}}$		—	48	—	pF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		—	22	—	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=325\text{V}, \text{I}_D=5\text{A},$ $\text{R}_G=10\Omega$	—	11.8	—	ns
Turn-On Rise Time	$t_r$		—	1.2	—	ns
Turn-Off Delay Time	$t_{\text{d(off)}}$		—	21.2	—	ns
Turn-Off Fall Time	$t_f$		—	5.8	—	ns
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=520\text{V}, \text{I}_D=5\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$	—	13	—	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		—	6.7	—	nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		—	2	—	nC
<b>Drain-Source Body Diode Characteristics and Maximum Ratings</b>						
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_S=6\text{A}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_S=1\text{A},$ $d\text{I}_F/dt=100\text{A/us}, (\text{Note}3)$	—	149	—	ns
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$		—	372	—	nC

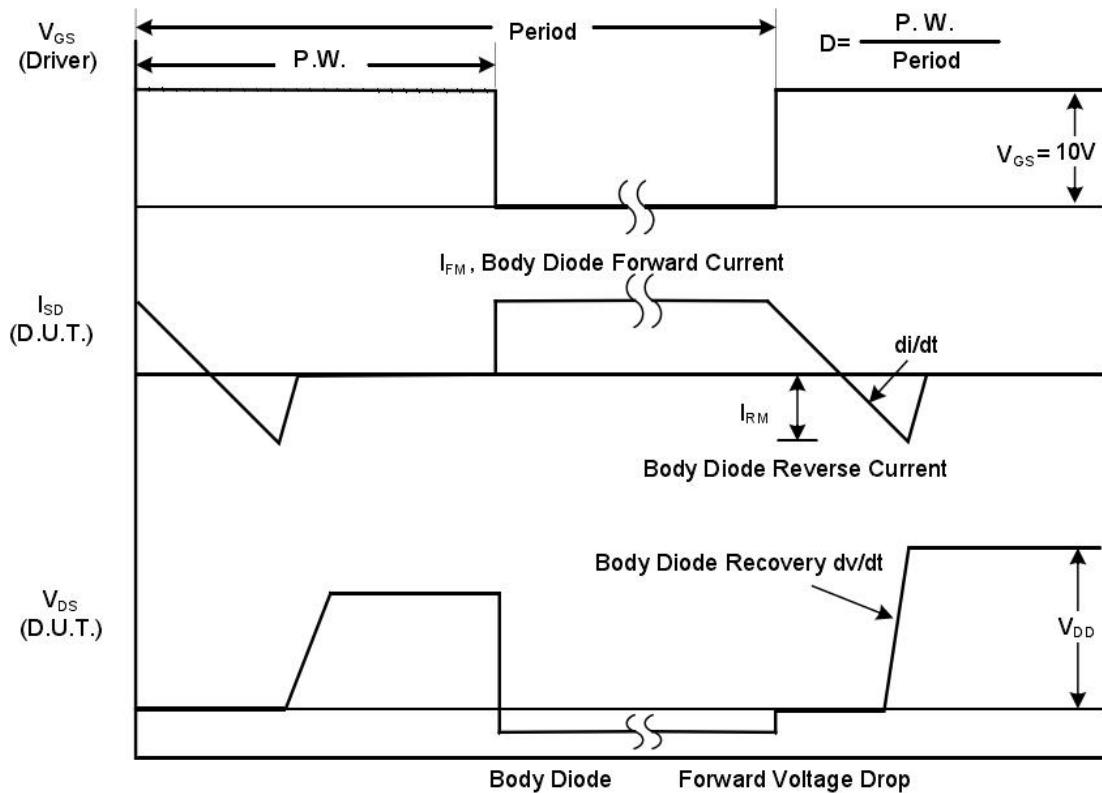
### Notes

- Repetitive Rating:pulse width limited by maximum junction temperature.
- $\text{V}_{\text{DD}}=50\text{V}, L=10\text{mH}, R_g=25\Omega$ , starting  $T_J=25^\circ\text{C}$ .
- Pulse width $\leq 300\text{us}$ ; duty cycle $\leq 2\%$ .

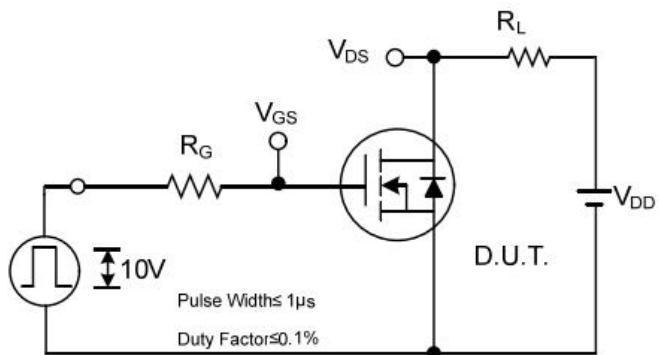
## RATING AND CHARACTERISTIC CURVES



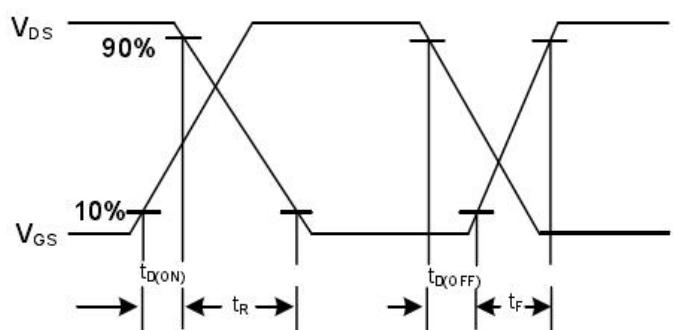
Peak Diode Recovery dv/dt Test Circuit



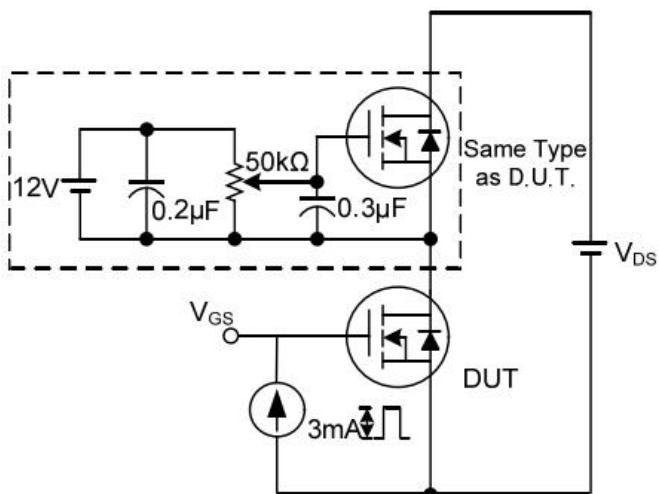
Peak Diode Recovery dv/dt Waveforms



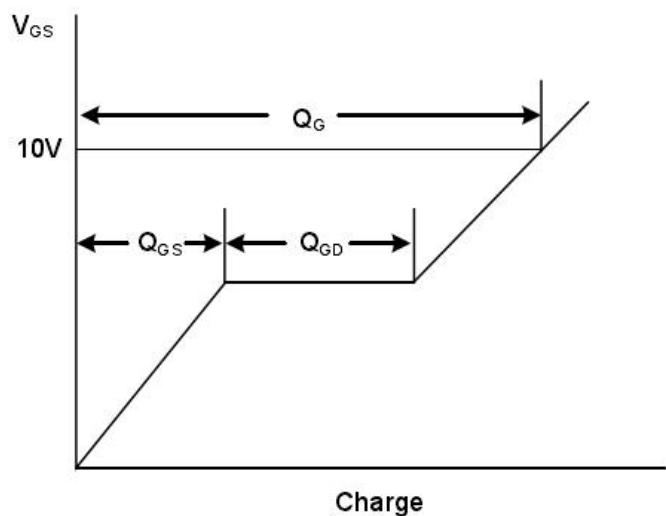
Switching Test Circuit



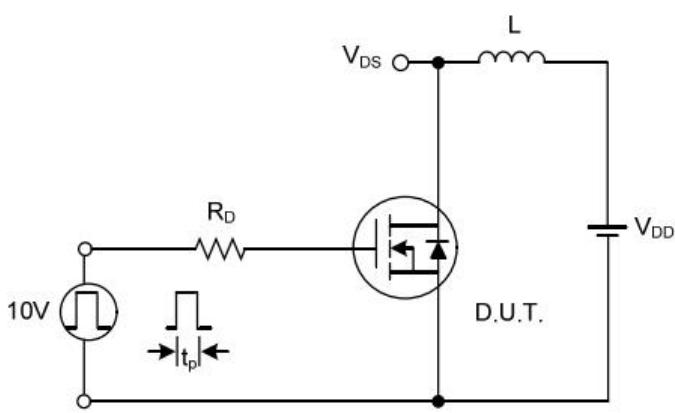
Switching Waveforms



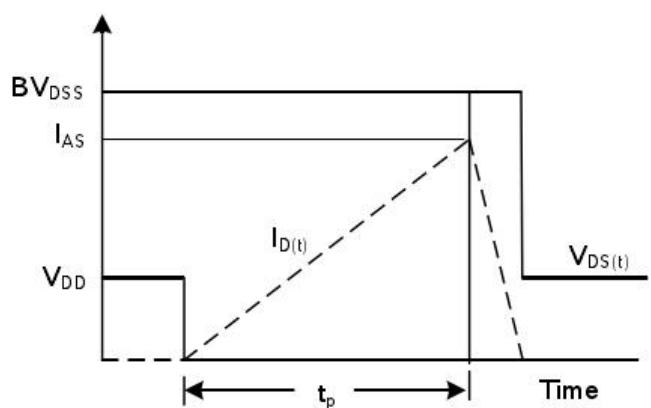
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

## RATING AND CHARACTERISTIC CURVES

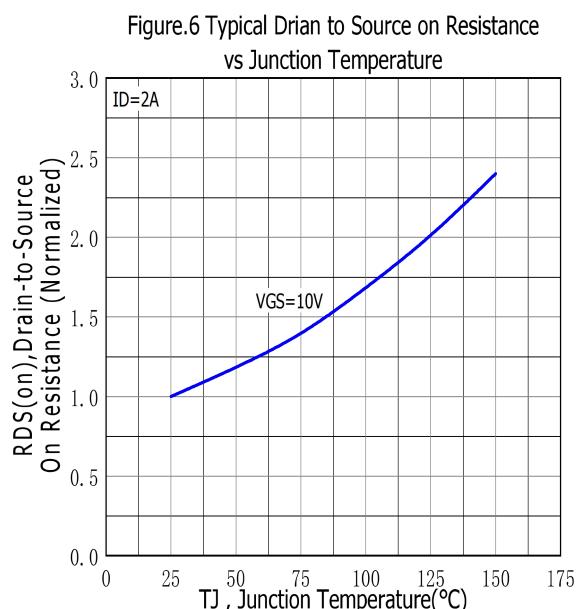
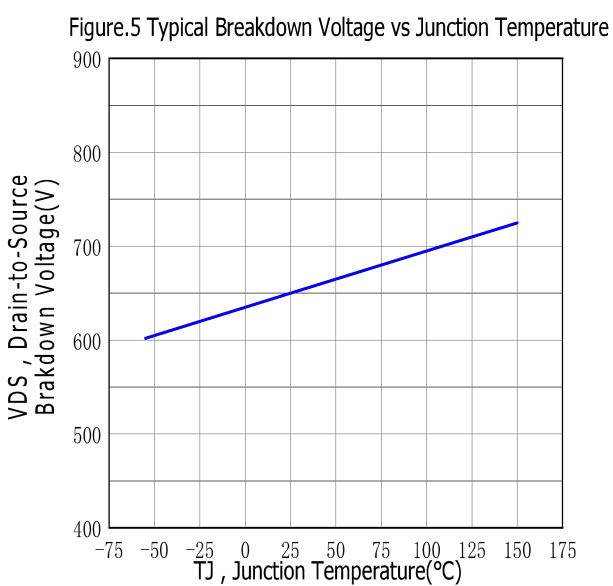
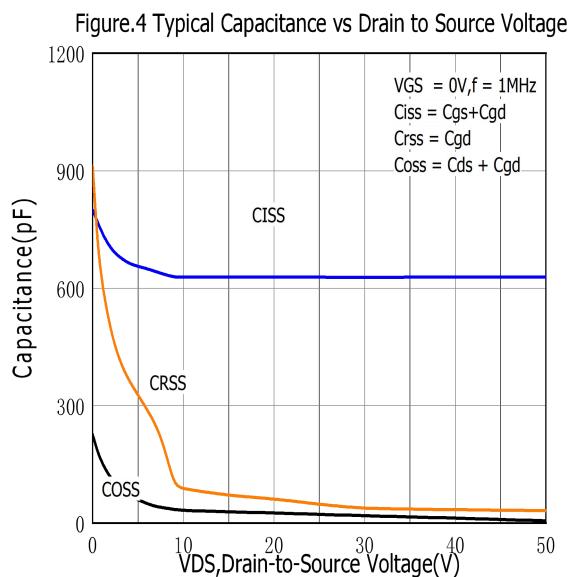
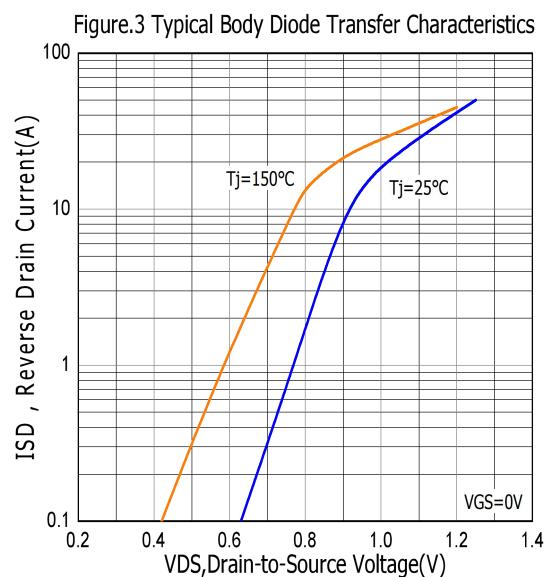
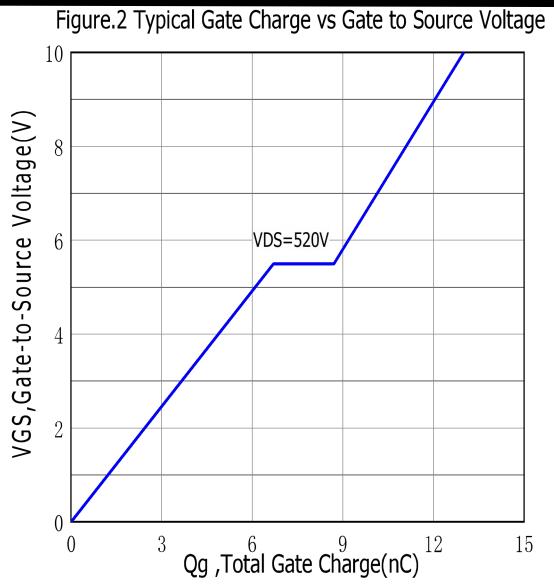
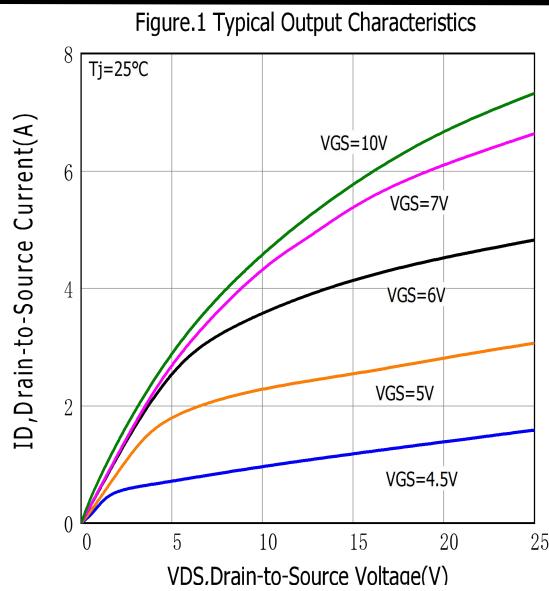


Figure.7 Maximum Forward Bias Safe Operating Area

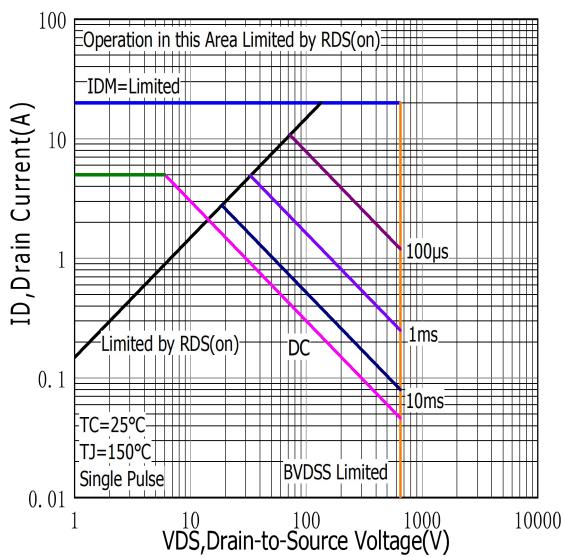


Figure.9 Maximum EAS vs Channel Temperature

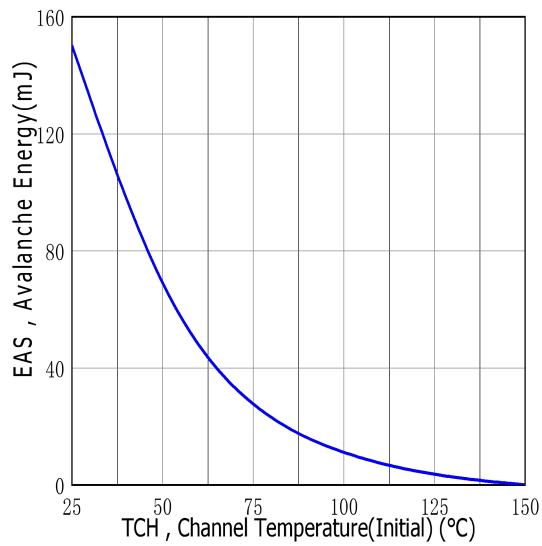


Figure.11 Maximum Effective Thermal Impedance , Junction to Case

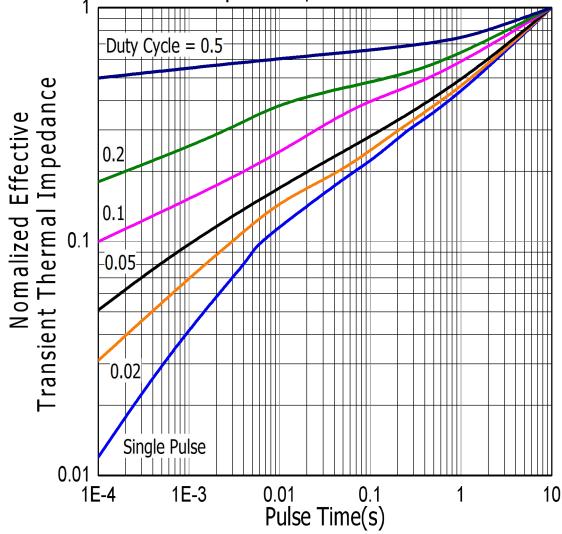


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

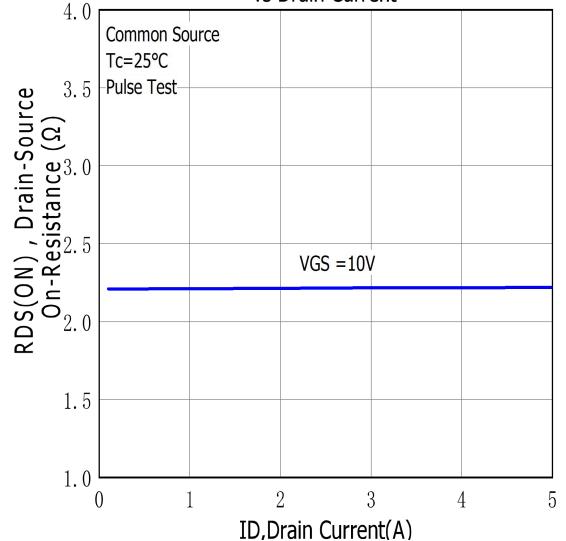


Figure.10 Typical Threshold Voltage vs Case Temperature

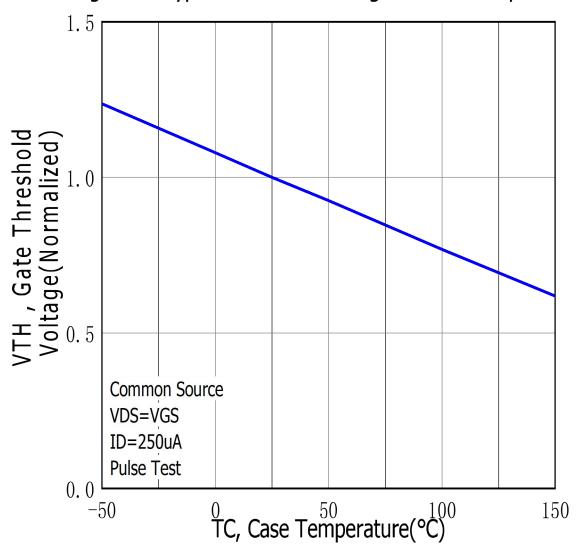
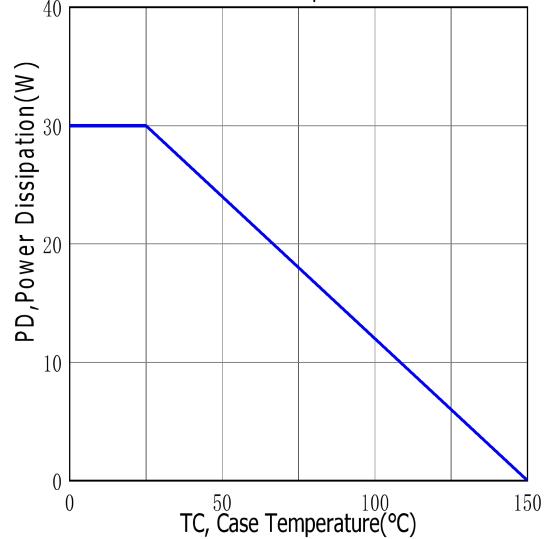
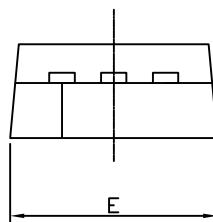
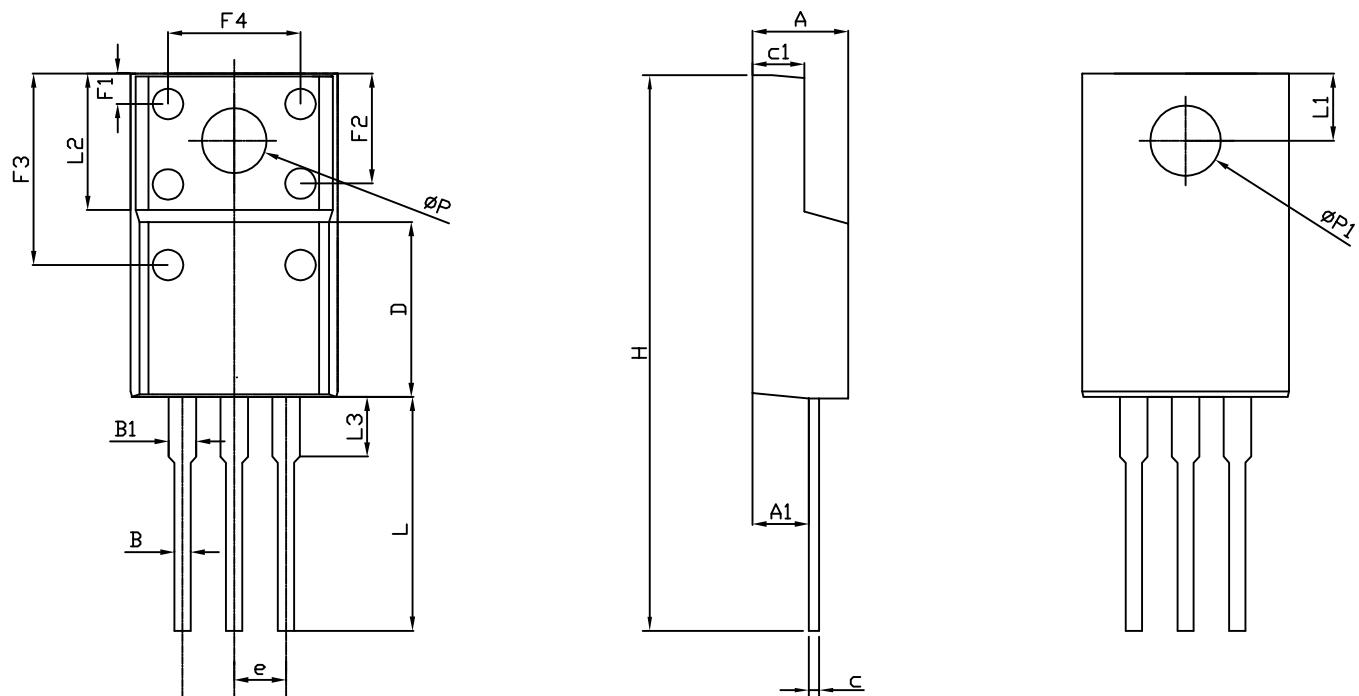


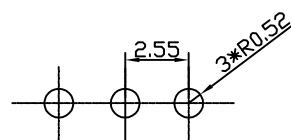
Figure.12 Maximum Power Dissipation vs Case Temperature



# TO-220F-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	4.40	4.60	4.80
A1	2.63	2.76	2.89
B	0.75	0.80	0.90
B1	1.12	1.27	1.42
c	0.40	0.50	0.60
c1	2.60	2.70	2.80
D	7.50	7.80	8.10
e	-	2.55REF	-
E	9.86	10.00	10.10
F1	1.90	2.12	2.40
F2	5.00	5.30	5.65
F3	8.70	9.00	9.30
F4	6.20	6.50	6.80
H	27.80	28.30	28.80
L	13.10	13.30	13.50
L1	2.85	3.00	3.15
L2	-	6.70REF	-
L3	2.80	3.10	3.40
ΦP	3.00	3.30	3.60
ΦP1	2.80	3.10	3.40