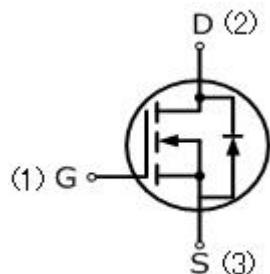


## 180N10C

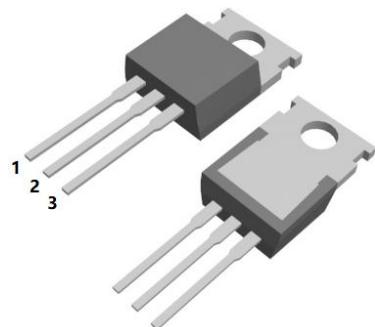
180Amps, 100 Volts N-CHANNEL MOSFET

### FEATURE

- 180A, 100V,  $R_{DS(ON)MAX}=7.0\text{ m}\Omega$ ,  $V_{GS}=10\text{ V}/20\text{ A}$
- Low gate charge
- Low  $C_{iss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS 2.0 Compliant



TO-220CB-3L



### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	180N10C	UNIT
Drain-Source Voltage	$V_{DSS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	180	A
Pulsed Drain Current (Note 1)	$I_{DM}$	720	
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	700	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +175	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	260	°C

### Thermal Characteristics

Parameter	Symbol	MAX	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	0.4	°C/W
Maximum Power Dissipation	$T_c=25^\circ\text{C}$	375	W

Electrical Characteristics ( $T_c=25^\circ\text{C}$ ,unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	100	—	—	V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	$\mu\text{A}$
Gate-Body Leakage Current,Forward	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	$\pm 100$	nA
<b>On Characteristics</b>						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	2	—	4	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=20\text{A}$	—	5.0	7.0	$\text{m}\Omega$
Gate Resistance	$\text{R}_g$	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}} \text{ Open}, f=1\text{MHz}$	—	2.0	5.0	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	—	5977	—	pF
Output Capacitance	$\text{C}_{\text{oss}}$		—	1424	—	pF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		—	51	—	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=50\text{V}, \text{I}_D=20\text{A}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_{\text{GEN}}=3\Omega$	—	39	—	ns
Turn-On Rise Time	$t_r$		—	32	—	ns
Turn-Off Delay Time	$t_{\text{d(off)}}$		—	86	—	ns
Turn-Off Fall Time	$t_f$		—	46	—	ns
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=50\text{V}, \text{I}_D=30\text{A}$	—	108	—	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		—	29	—	nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		—	40	—	nC
<b>Drain-Source Body Diode Characteristics and Maximum Ratings</b>						
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_S=20\text{A}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1.3	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{I}_F=50\text{A}, \frac{d\text{I}_F}{dt}=100\text{A/us}, (\text{Note}3)$	—	66	—	ns
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$		—	125	—	nC

### Notes

1. Repetitive Rating:pulse width limited by maximum junction temperature.
2.  $L=0.5\text{mH}$ , starting  $T_j=25^\circ\text{C}$ .
3. Pulse width $\leq 300\text{us}$ ;duty cycle $\leq 2\%$ .

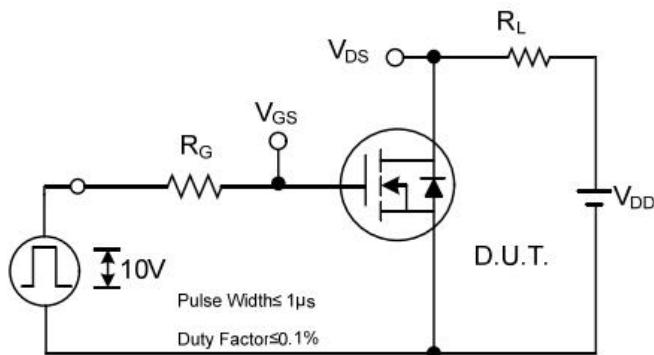
## RATING AND CHARACTERISTIC CURVES



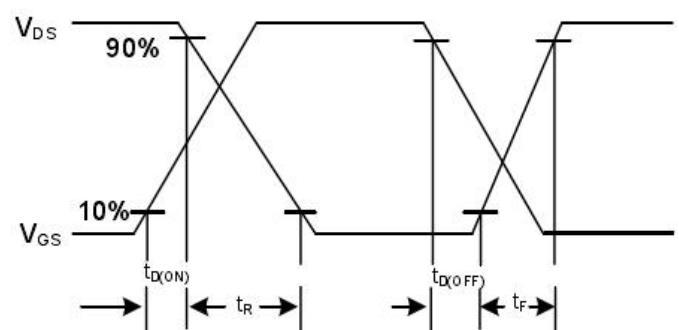
Peak Diode Recovery  $dv/dt$  Test Circuit



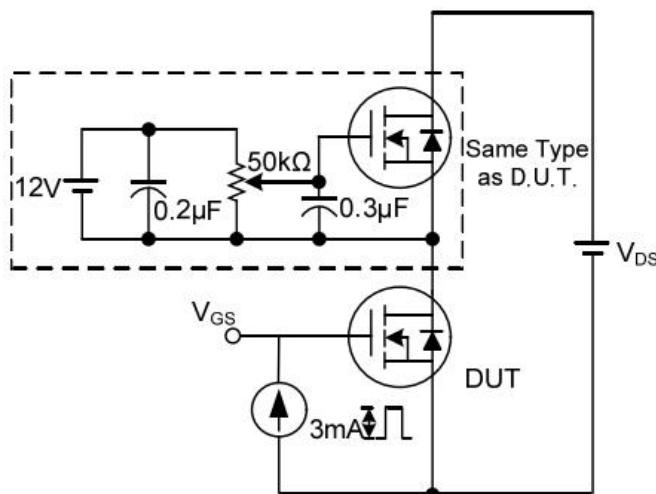
Peak Diode Recovery  $dv/dt$  Waveforms



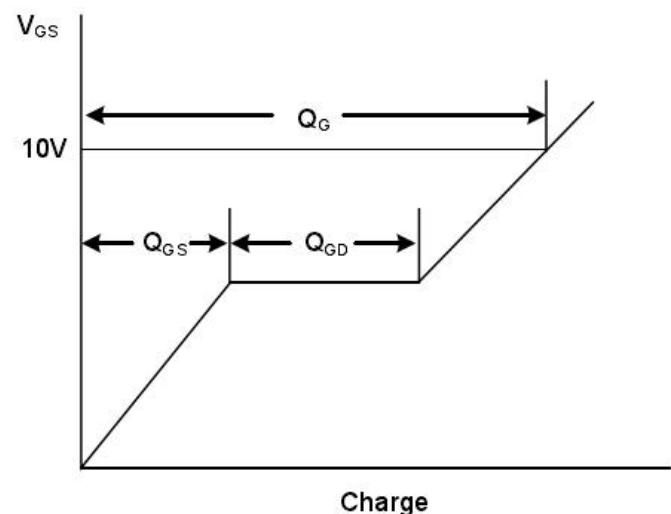
Switching Test Circuit



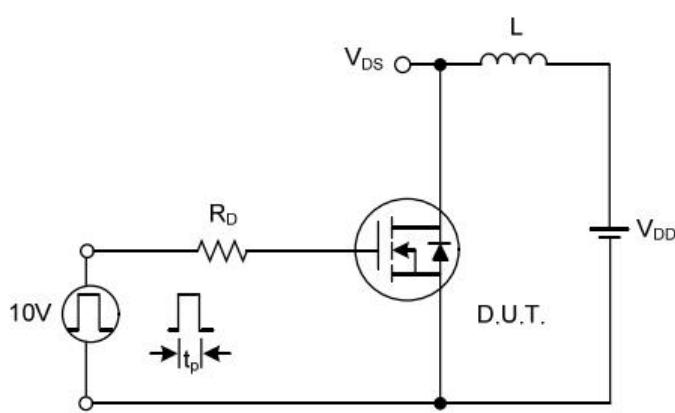
Switching Waveforms



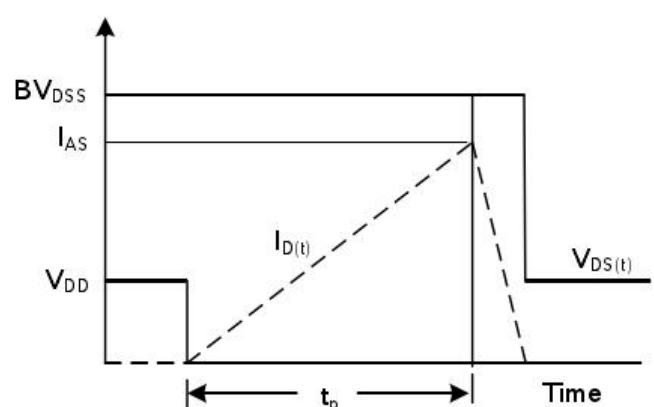
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

## RATING AND CHARACTERISTIC CURVES

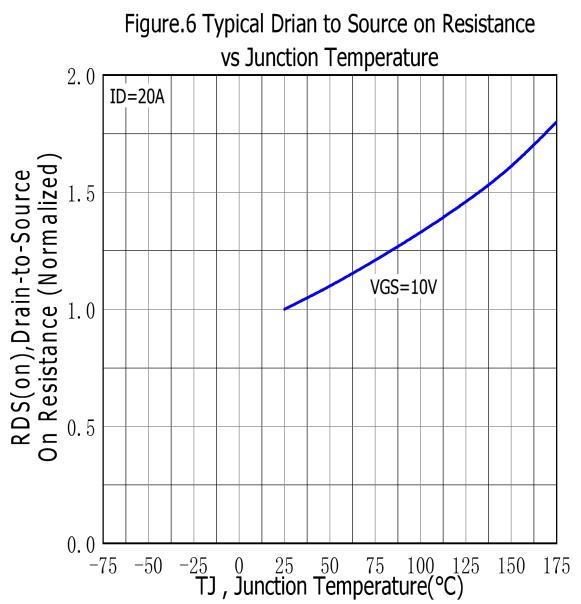
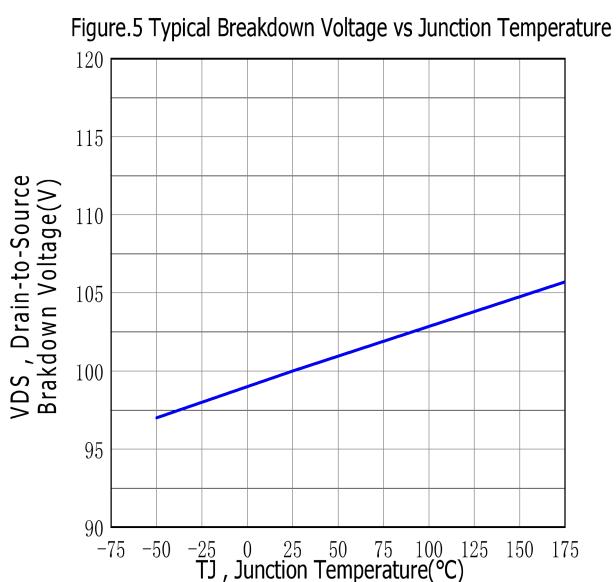
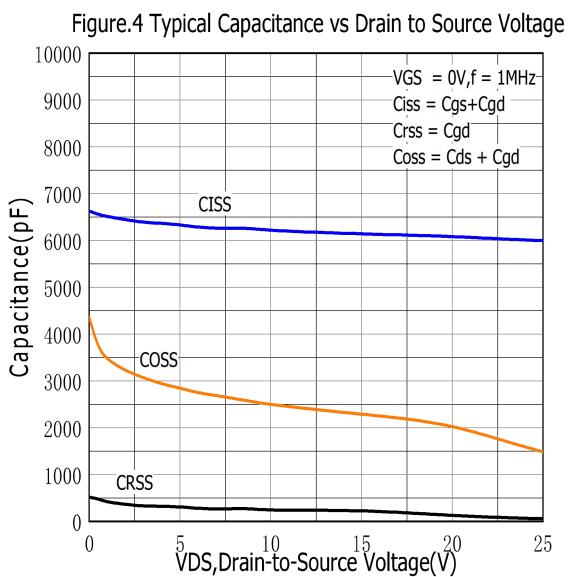
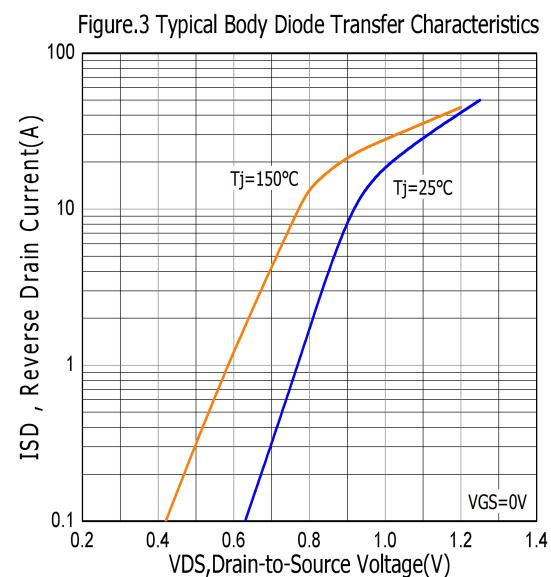
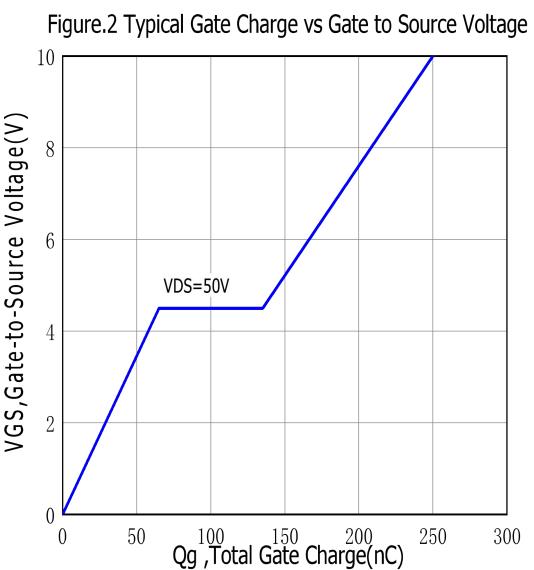
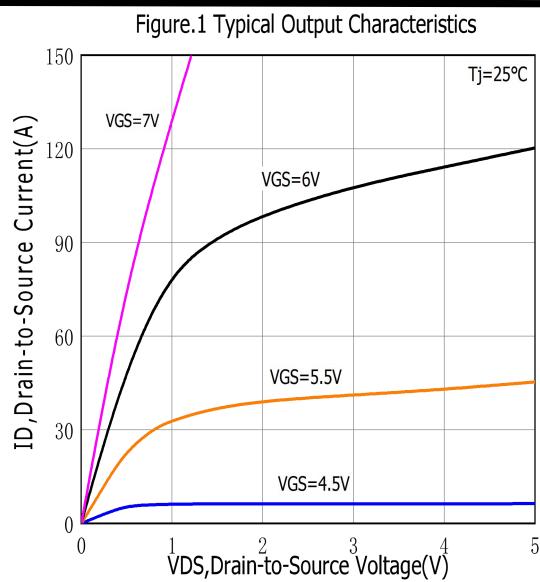


Figure.7 Maximum Forward Bias Safe Operating Area

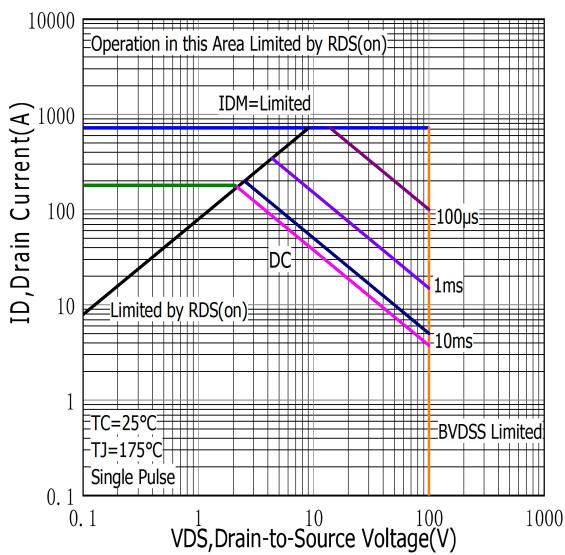


Figure.9 Maximum EAS vs Channel Temperature

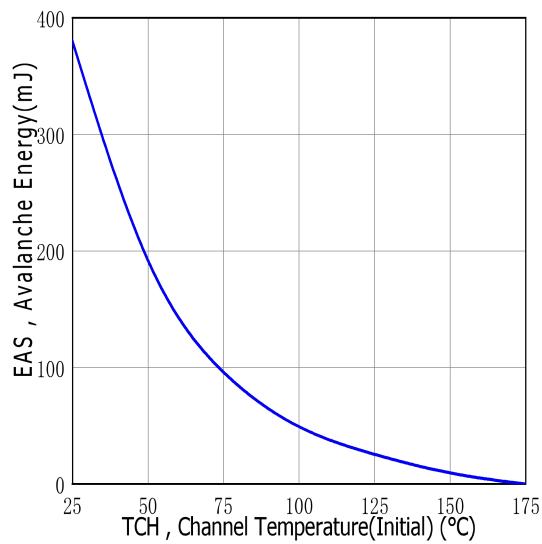


Figure.11 Maximum Effective Thermal Impedance , Junction to Case

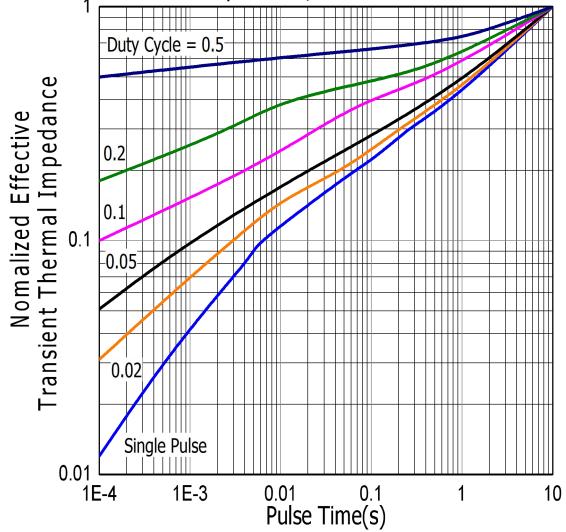


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

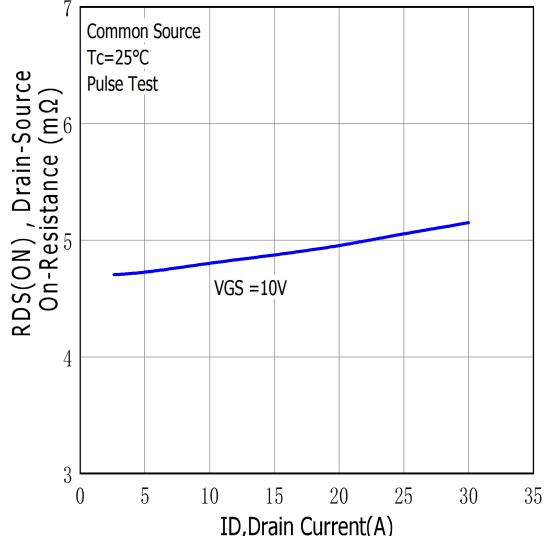


Figure.10 Typical Threshold Voltage vs Case Temperature

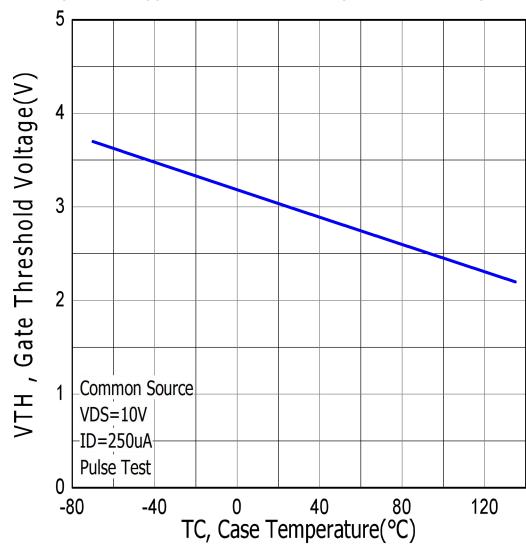
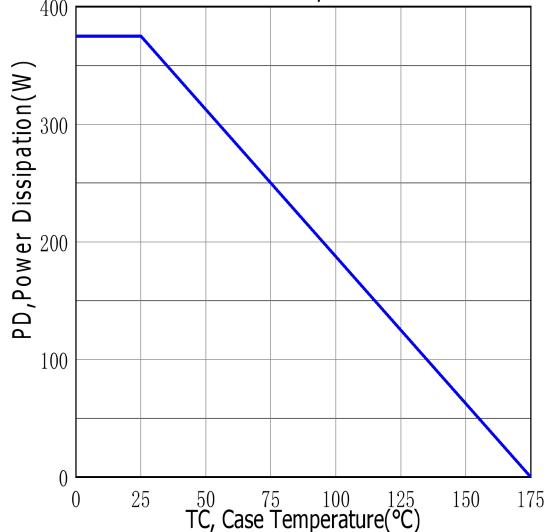
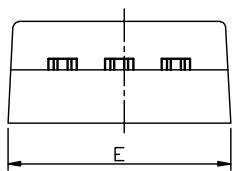
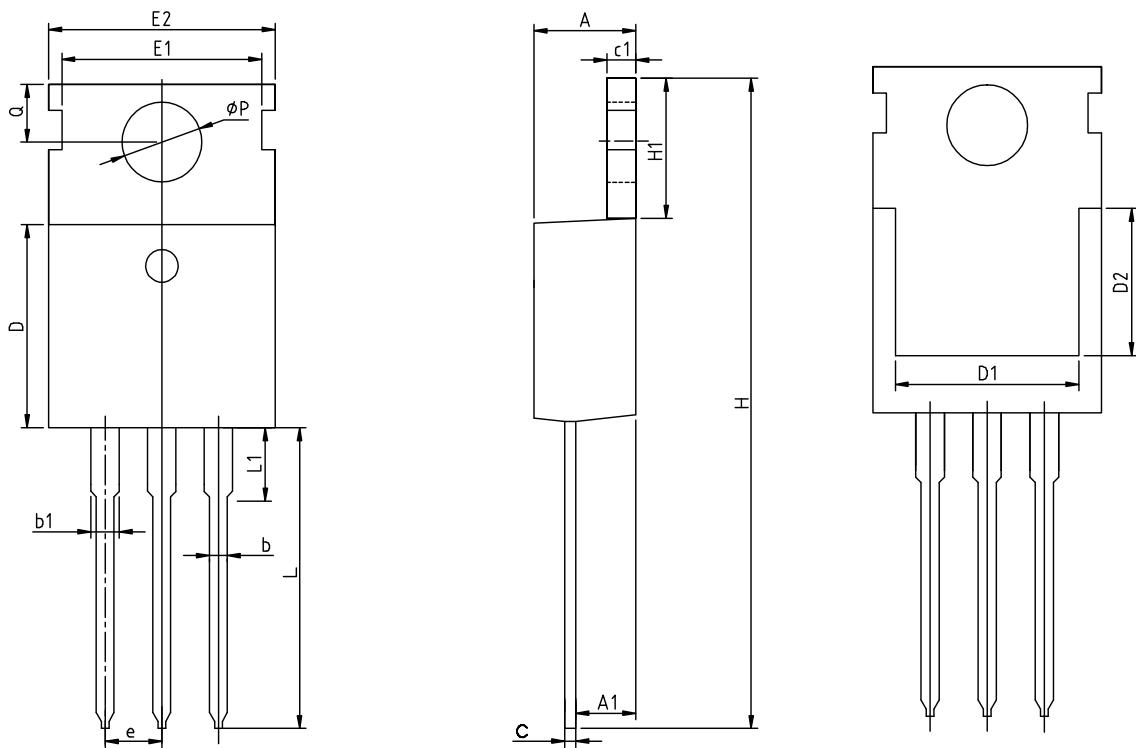


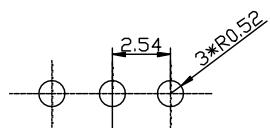
Figure.12 Maximum Power Dissipation vs Case Temperature



## TO-220CB-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	4.40	4.60	4.80
A1	2.25	2.40	2.55
b	0.72	0.82	0.92
b1	1.12	1.27	1.42
c	0.40	0.50	0.60
c1	1.20	1.30	1.40
D	8.80	9.10	9.40
D1	7.75	7.95	8.15
D2	6.55	6.75	6.95
e		2.54BSC	
E	9.65	10.00	10.35
E1		8.70	
E2	9.70	10.00	10.30
H	28.70	29.20	29.70
H1	6.25	6.50	6.85
L	13.20	13.50	13.80
L1	2.80	3.10	3.40
Q	2.60	2.80	3.00
ΦP	3.45	3.60	3.75