

PW35K

30V N-Channel MOSFET

0.6A 30V; $R_{DS(ON)typ}=320m\Omega@4.5V$, $R_{DS(ON)typ}=410m\Omega@2.5V$

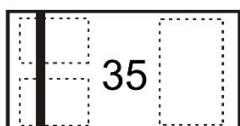
FEATURE

- Surface Mount Package
- N-Channel Switch with Low RDS(on)
- Operated at Low Logic Level Gate Drive

Application

- Load/Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable Electronics
- Logic Level Shift

MARKING:



Top View
Bar Denotes Gate
and Source Side

DFN1006-3L

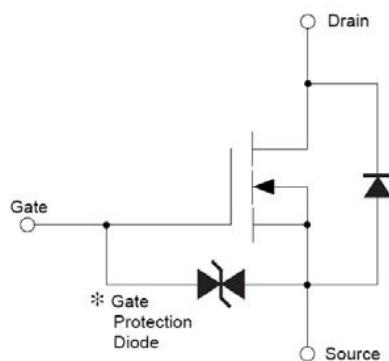


Bottom View



Top View
Internal Schematic

Schematic diagram



ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	0.6	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	1.8	A
Power Dissipation ⁽²⁾	P_D	100	mW
Thermal Resistance from Junction to Ambient ⁽¹⁾	$R_{\theta JA}$	1250	$^\circ C/W$
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature	T_{STG}	-55~+150	$^\circ C$

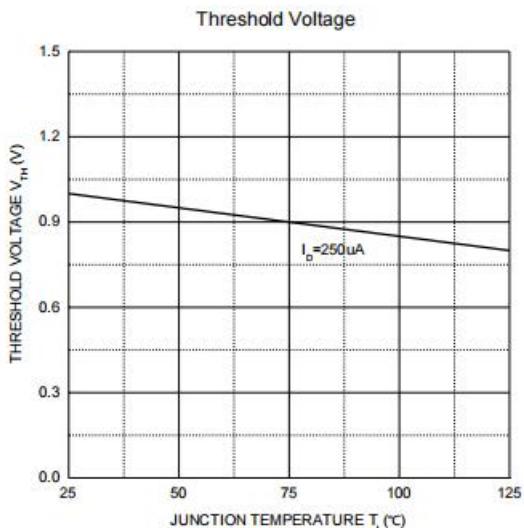
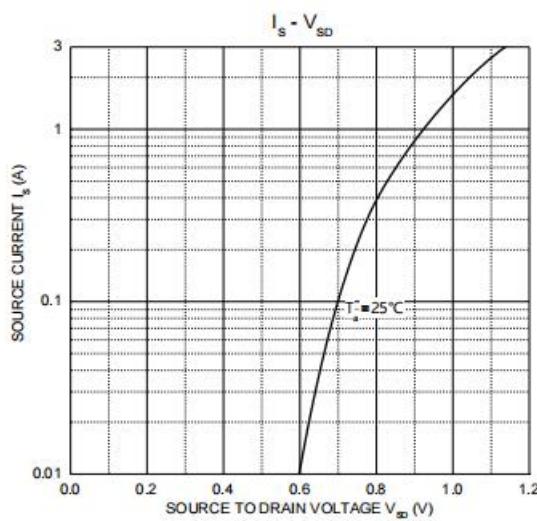
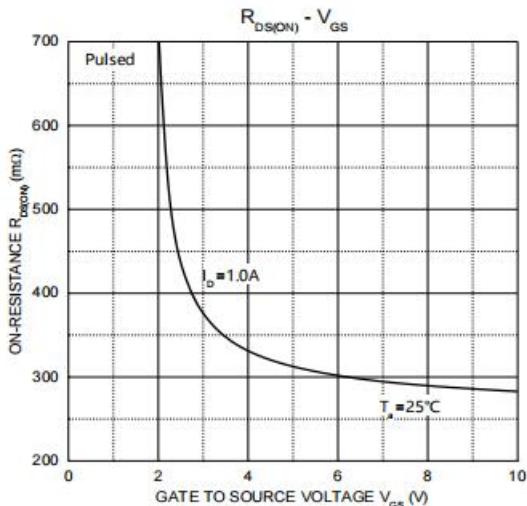
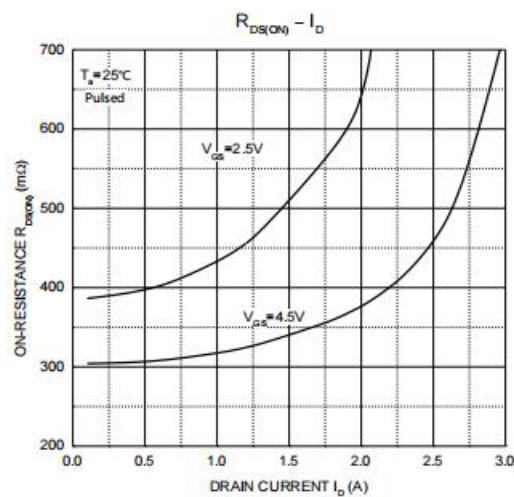
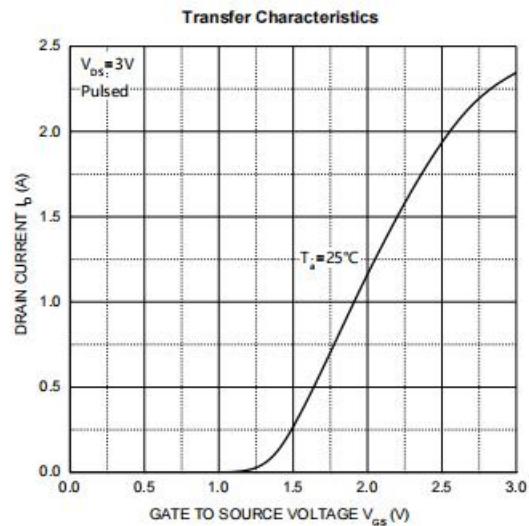
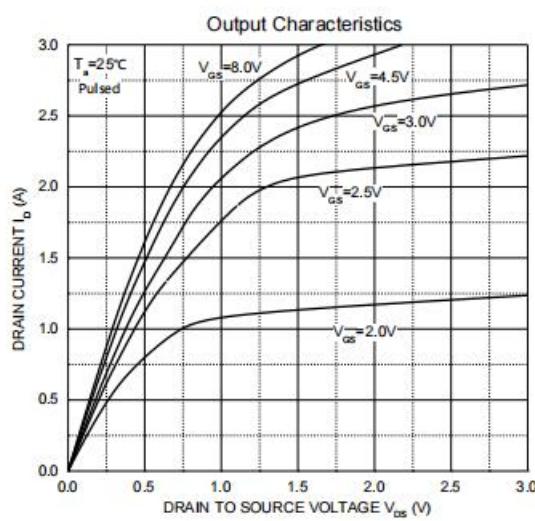
MOSFET ELECTRICAL CHARACTERISTICS($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 10\text{V}, V_{\text{DS}} = 0\text{V}$			± 3	μA
Gate threshold voltage ⁽³⁾	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.5	1.0	1.25	V
Drain-source on-resistance ⁽³⁾	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 0.6\text{A}$		320	430	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 0.3\text{A}$		410	600	
Forward transconductance	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 0.5\text{A}$	0.1			S
Dynamic characteristics⁽⁴⁾						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		44		pF
Output Capacitance	C_{oss}			15		
Reverse Transfer Capacitance	C_{rss}			8		
Total gate charge	Q_g	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 4.5\text{V}, I_D = 0.8\text{A}$		1.2		nC
Gate-source charge	Q_{gs}			0.28		
Gate-drain charge	Q_{gd}			0.3		
Switching Characteristics⁽⁴⁾						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 15\text{V}, I_D = 0.7\text{A}, V_{\text{GS}} = 4.5\text{V}, R_G = 51\Omega$		5.0		ns
Turn-on rise time	t_r			8.2		
Turn-off delay time	$t_{\text{d}(\text{off})}$			23		
Turn-off fall time	t_f			41		
Source-Drain Diode characteristics						
Diode Forward voltage ⁽³⁾	V_{DS}	$I_S = 0.6\text{A}, V_{\text{GS}} = 0\text{V}$		0.87	1.2	V

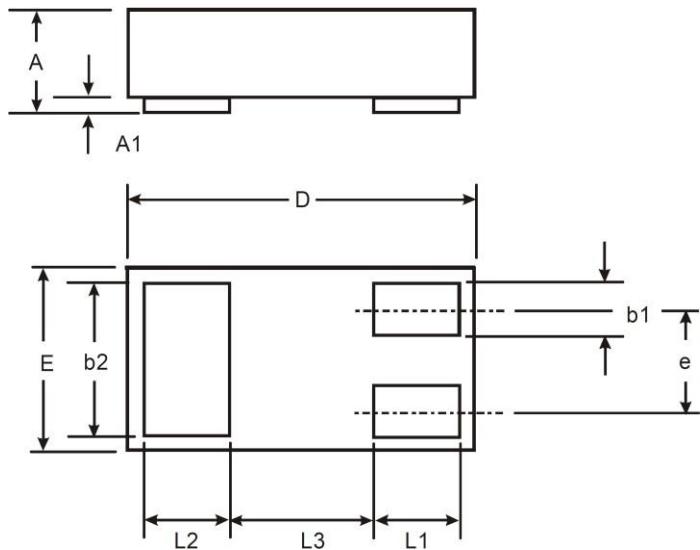
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. This test is performed with no heat sink at $T_a=25^\circ\text{C}$.
3. Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.
4. These parameters have no way to verify.

Typical Electrical and Thermal Characteristics



DFN1006-3L Package Information

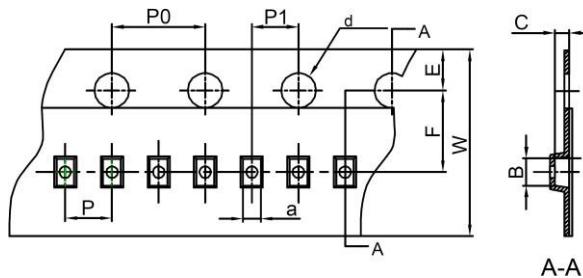


X1-DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b1	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	—	—	0.40

All Dimensions in mm

DFN1006-3L Tape and Reel

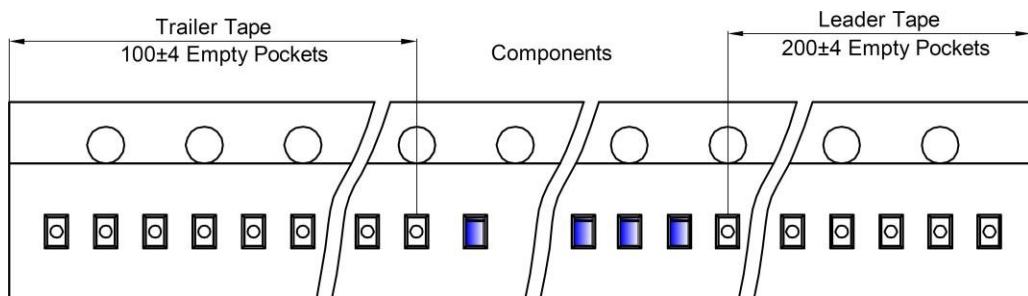
DFN1006-3L Embossed Carrier Tape



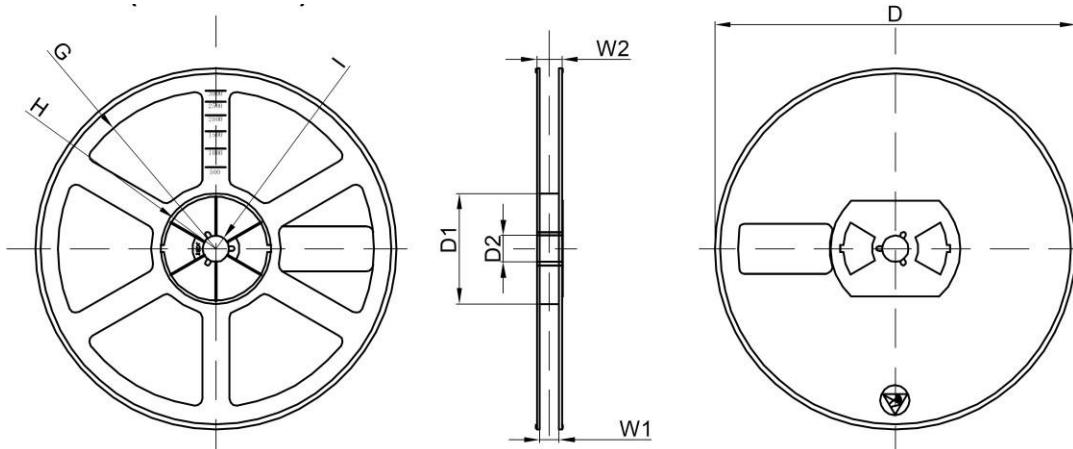
Dimensions are in millimeter

Pkg type	a	B	C	d	E	F	P0	P	P1	W
DFN1006-3L	0.66	1.15	0.66	Ø1.50	1.75	3.50	4.00	2.00	2.00	8.00

DFN1006-3L Tape Leader and Trailer



DFN1006-3L Reel



Dimensions are in millimeter

Reel Option	D	D1	D2	G	H	I	W1	W2
7" Dia	Ø178.00	54.40	13.00	R78.00	R25.60	R6.50	9.50	12.30

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
10000 pcs	7 inch	100,000 pcs	203×203×195	400,000 pcs	438×438×220	