

PW72K

60V N-Channel MOSFET

0.41A 60V; $R_{DS(ON)typ}=1.2\Omega@10V$, $R_{DS(ON)typ}=1.3\Omega@4.5V$

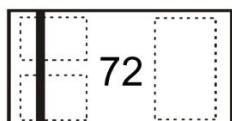
FEATURE

- Low On-Resistance
- Low Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate

Application

- Load Switch
- Portable Applications
- Power Management Functions

MARKING:

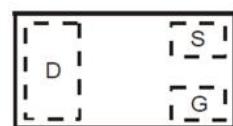


Top View
Bar Denotes Gate
and Source Side

DFN1006-3L

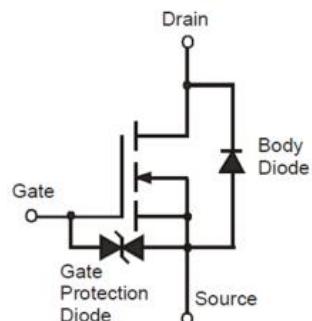


Bottom View



Top View
Internal Schematic

Schematic diagram



ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	0.41	A
		0.30	
Pulsed Drain Current	I_{DM}	1.2	A
Power Dissipation	P_D	0.1	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	1250	$^\circ C/W$
Storage Temperature	T_{STG}	-55~+150	$^\circ C$

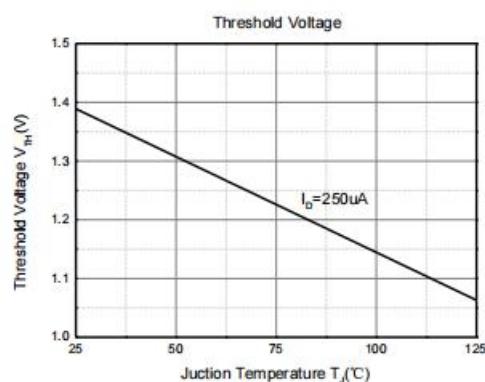
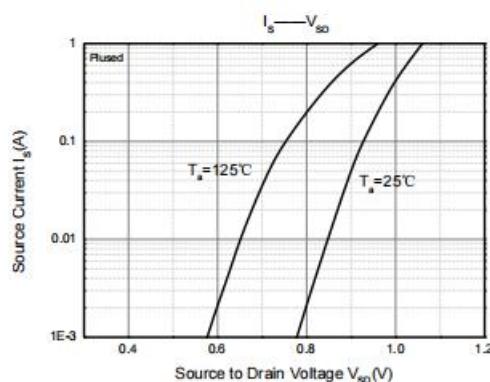
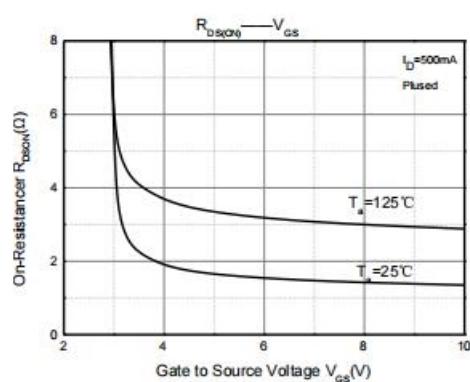
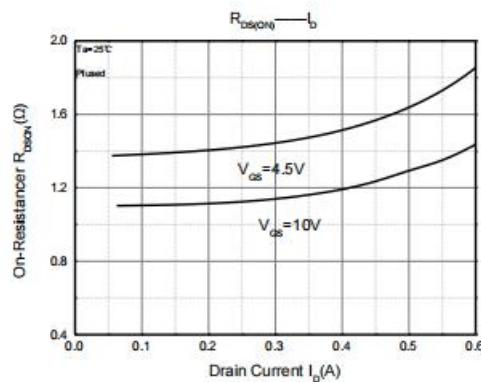
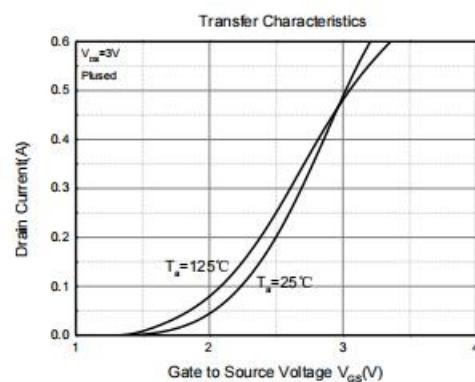
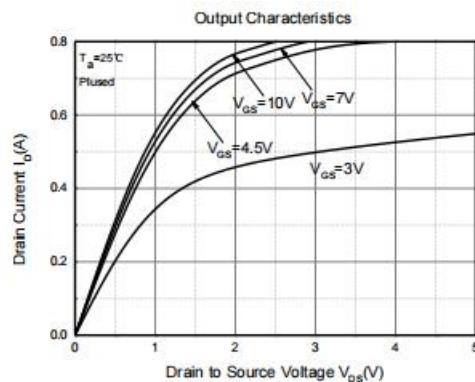
MOSFET ELECTRICAL CHARACTERISTICS($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
STATIC CHARACTERISTICS						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	60			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}$			100	nA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 10	μA
		$V_{\text{GS}} = \pm 5\text{V}, V_{\text{DS}} = 0\text{V}$			± 1	
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.0	1.4	2.5	V
Drain-source on-resistance ^(a)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 40\text{mA}$		1.2	1.5	Ω
		$V_{\text{GS}} = 4.5\text{V}, I_D = 35\text{mA}$		1.3	1.8	
Forward tranconductance ^(a)	g_{FS}	$V_{\text{DS}} = 5\text{V}, I_D = 40\text{mA}$	100			mS
Diode Forward Voltage	V_{SD}	$I_S = 300\text{mA}, V_{\text{DS}} = 0\text{V}$		0.84	1.1	V
DYNAMIC CHARACTERISTICS						
Input Capacitance ^(b)	C_{iss}	$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		41	80	pF
Output Capacitance ^(b)	C_{oss}			3.6	7	
Reverse Transfer Capacitance ^(b)	C_{rss}			2.9	5.6	
Gate resistance	R_g	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		81	200	Ω
Total Gate Charge	Q_g	$V_{\text{GS}} = 4.5\text{V}$	$V_{\text{DS}} = 50\text{V}, I_D = 1\text{A}$	0.72	1.5	nC
		$V_{\text{GS}} = 10\text{V}$		1.41	2.8	
Gate-Source Charge	Q_{gs}			0.24	0.4	
Gate-Drain Charge	Q_{gd}			0.24	0.5	
Turn-on delay time ^(b)	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 50\text{V}, I_D = 1\text{A}, R_G = 6\Omega$		3.98	10	ns
Turn-on rise time ^(b)	t_r			4.95	10	
Turn-off delay time ^(b)	$t_{\text{d}(\text{off})}$			18.52	40	
Turn-off fall time ^(b)	t_f			11.94	25	

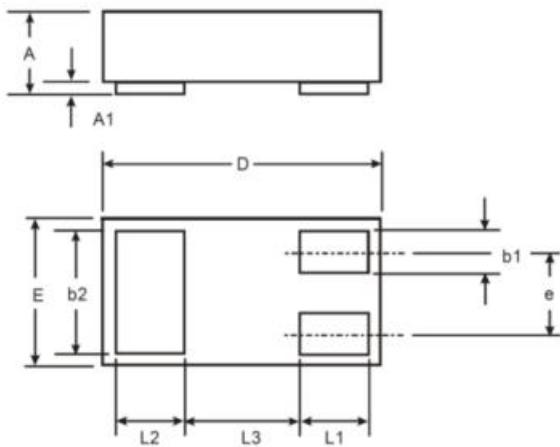
Notes:a. Pulse Test ; Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

b. These parameters have no way to verify.

Typical Electrical and Thermal Characteristics



DFN1006-3L Package Information

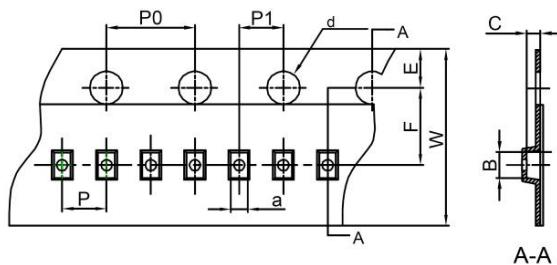


X1-DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b1	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	—	—	0.40

All Dimensions in mm

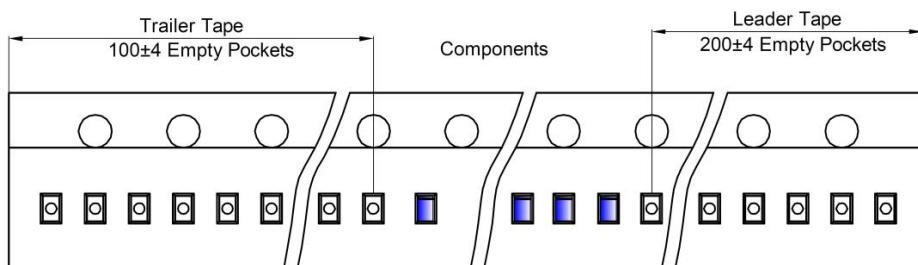
DFN1006-3L Tape and Reel

DFN1006-3L Embossed Carrier Tape

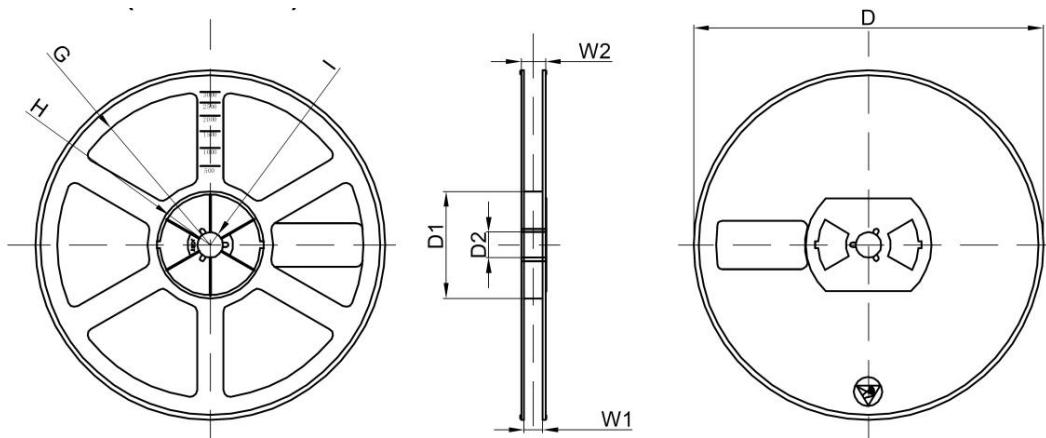


Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
SOT-323	0.66	1.15	0.66	Ø1.50	1.75	3.50	4.00	2.00	2.00	8.00

DFN1006-3L Tape Leader and Trailer



DFN1006-3L Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
7" Dia	Ø178.00	54.40	13.00	R78.00	R25.60	R6.50	9.50	12.30

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
10000 pcs	7 inch	100,000 pcs	203×203×195	400,000 pcs	438×438×220	